

AF
2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gregory A. Overkamp et al. Art Unit : 2183
Serial No.: 09/675,816 Examiner : Charles A.
Assignee : Intel Corporation Harkness
Filed : September 28, 2000
Title : DIGITAL SIGNAL PROCESSOR WITH MULTIPLE INSTRUCTION
DESTINATIONS FOR A PIPELINED SYSTEM

MAIL STOP PGPUB-DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

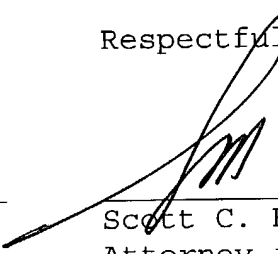
TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed four (4) sheets of formal drawings for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account
No. 06-1050.

Respectfully submitted,

Date: June 3, 2004



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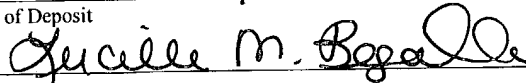
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